

IN THE SPECIFICATION:

Please amend the specification as follows.

Please amend Paragraph [0003] on Page 1 to read as follows:

[0003] ~~The object of the invention is to provide an improved method and an improved circuit arrangement.~~ One aspect of the invention is a method for processing signals in a radio system receiver and/or transmitter. The method comprises first determining for different circuit arrangement nodes at least one operation to execute, second determining one or more division criteria for signal classes, dividing at least one of the signals or signal components according to the one or more division criteria for signal classes, executing the predetermined operations in the circuit arrangement nodes signal-classwise.

Please amend Paragraph [0004] on Page 1 to read as follows:

[0004] One aspect of the invention is a method for processing signals in a radio system receiver and/or transmitter. The method comprises first determining for different circuit arrangement nodes at least one operation to execute and selecting a modification level from the circuit arrangement, merging the selected modification level nodes and deleting irrelevant nodes and links between the nodes and/or adding new links, second determining one or more division criteria for signal classes for dividing the signals or signal components, dividing at least one of the signals or signal components according to the one or more division criteria for signal classes, executing the predetermined operations in the circuit arrangement nodes signal-classwise.

Please amend Paragraph [0005] on Page 2 to read as follows:

[0005] One aspect of the invention is a circuit arrangement for signal processing in a radio system receiver and/or transmitter, the circuit arrangement comprising: ~~Nodes of~~ the circuit arrangement ~~are~~ nodes arranged to execute at least one operation, ~~the circuit arrangement comprising~~ means for dividing at least one of the signals or signal components according to the one or more predetermined division criteria for signal classes, ~~the circuit arrangement comprising~~ means for executing the predetermined operations signal-classwise.